**Resume**

Srikanth Devapooja

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**CAREER OBJECTIVE**

* Looking for a challenging & responsible position to enhance my skills, willing to work in an environment that offers growth and exposure to the latest technologies where I can explore myself and realize my potential.

**CAREER SUMMARY**

* QA Engineer supporting the continued design, development, quality applications for Telecom products.
* Good knowledge on validation of product testing and able to understand the schematic to debug any hardware level issues.
* Experienced with different communication protocols such as RS232, I2C, SPI, and TCP/IP.
* Familiar to lab instruments like Multimeter, Oscilloscope, OPM, LCR Meter, Logic analyzer, Optical Spectrum analyzer, 1G/10G JDSU optical fiber tester , DCA etc & good soldering, wiring skills.
* Ability to derive Test plan, Test cases, debug and analyze issues.
* Good experiences in Performing Regression Testing to verify and uncover errors.
* Gained knowledge on embedded concepts and programming languages.
* Experienced on testing, troubleshooting of electronic boards via circuit diagram.
* Experience on Schematics design, board debugging.
* Worked on both Client Projects and Academic Projects.
* Verification and validation of test equipment co-ordination with Technicians, wireman & fitters to get production done according to daily targets.

**WORK EXPERIENCE**

* Arunodaya Electro Power Technologies.

Embedded Engineer.

May 2017 - June 2018.

* United Telecoms Limited

Engineer - R&D.

Jan 2015 - May 2017.

**TECHNOLOGIES AND CONCEPTS USED**

* 10G DWDM Optical System, SFP transceiver module
* GPON/FTTH, ONT, OLT System, Optical splitter, Gigabit Ethernet, VoIP Server, VLAN port Tagging, Media converters.

**TRAINING ATTENDED**

* Computer Hardware and Networking from NIMSME.
* Completed course on Real time projects using software automation tools.
* DCA from Lotus Software Technologies.

**ACADEMICS**

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| --- | --- | --- | --- |
| **Qualification** | **College/School** | **Year** | **Percentage** |
| B.Tech (ECE) | Holy Mary Institute of Technology and Science | 2014 | 69.2 |
| Intermediate | Sri Chaitanya Junior Kalasala | 2010 | 77.7 |
| SSC | Sandeepam Vidyalaya High School | 2008 | 63.1 |

**TECHNICAL SKILLS**

* Programming languages : C, Java, HTML
* Operating Systems : Windows Family, Linux.
* Designing Tools : ORCAD 10.5, KiCAD.
* Editors/Compilers : Keil, Eclipse
* Documentation Tools : MS-Office.
* Network analyzer Tool : Wireshark

**PERSONAL DETAILS**

Date of Birth : 1st June 1992

Father’s name : D.Swamy

Languages known : Telugu, Hindi, English

Address : 4-1-121/22/B, VST Colony, Nacharam, Hyderabad -500076.

**DECLARATION**

I hereby declare that above mentioned information is true & correct to the best of my knowledge and I belief.

**Place:  Hyderabad**

**Date:**